



IDT Introduces Industry's First Fully JEDEC-Compliant Memory Buffer for DDR3 LRDIMM

New Memory Technology Dramatically Increases Performance of Memory Bound Applications Such as Cloud Computing and Virtualization

SAN JOSE, Calif.--(BUSINESS WIRE)-- Integrated Device Technology, Inc. ([IDT](#)[®])(NASDAQ: IDTI), the Analog and Digital Company delivering essential mixed-signal semiconductor solutions, today introduced the industry's first JEDEC-compliant Memory Buffer for DDR3 (MB3), to enable a new generation of DDR3 memory technology for servers, workstations and storage equipment. The IDT MB3 device dramatically increases server memory capacity, reliability, and scalability, critical bottlenecks in the performance of virtualized applications and services.

The IDT [MB301L](#) has been designed for use in load-reduced dual in-line memory modules (LRDIMMs) at up to DDR3-1600 speeds at 1.5V and 1.35V, delivering high performance and low power consumption in an integrated solution. The MB3 also delivers an unmatched suite of proprietary features, such as full source synchronous DRAM training algorithms, ultra-flexible pattern generation, error insertion/detection, and on-die scope capability via software. These features enhance performance, reliability and system testing.

LRDIMM is a new type of memory module being defined for the needs of next generation server applications, particularly Cloud Computing. Servers targeting the Cloud Computing model require highly scalable memory architectures to support the performance and elasticity needs of virtualized resources. LRDIMM technology delivers that scalability with a "pay as you go" approach to appropriately size a server's main memory pool as the user base evolves. Unlike RDIMMs, the performance of LRDIMMs does not significantly degrade as the number of DIMMs in a channel increases, maximizing the efficiency of a given set of CPU resources.

"LRDIMM is a critical technology to enhance server performance in virtualized environments such as Cloud Computing," said Mario Montana, vice president and general manager of the Enterprise Computing Division at IDT. "IDT is the industry leader in memory buffer technology dating back to FBDIMM, and that expertise and domain knowledge is applied to our MB3 products for DDR3. We have worked diligently with our customers, ecosystem partners and the JEDEC standards group to bring this new technology to fruition."

Unlike the DDR3 register used in RDIMM, which only buffers command, address and clock signals, the IDT MB301L manages all signals between the memory controller and SDRAM. By improving signal integrity both in the motherboard channel and on the LRDIMM, and effectively training the exchange of data to and from the DRAMs, the IDT MB301L allows higher densities of memory to be run at greater speeds and with fewer population restrictions than when using RDIMMs. The net result is the ability to run four or more times as much memory in a two-socket server at the highest speed grades supported.

Pricing and Availability

The IDT MB301L is sampling now in a MBG-588 package and is compatible with JEDEC LRDIMM modules designs. Contact your local sales or distribution representative for pricing. For more information, please visit www.idt.com/go/MB3.

[About IDT](#)

Integrated Device Technology, Inc., the Analog and Digital Company™, combines analog and digital technology to develop system-level innovations that optimize customers' applications and enrich the end-user experience. IDT uses its market leadership in timing, serial switching and interfaces, and adds analog and system expertise to provide complete application-optimized, mixed-signal solutions for the communications, computing and consumer segments. Headquartered in San Jose, Calif., IDT has design, manufacturing and sales facilities throughout the world. IDT stock is traded on the NASDAQ Global Select Stock Market® under the symbol "IDTI." Additional information about IDT is accessible at www.IDT.com. Follow IDT on [Facebook](#), [LinkedIn](#), [Twitter](#), and [YouTube](#).

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